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In modern digital design especially the very large scale integration VLSI circuits are designed utilizing n-MOS and p-MOS transistors connected as complimentary pair for power saving and fast access time.

Chapter 3 VLSI Design Concepts and Methodologies

This chapter examines the key device design issues in a modern CMOS VLSI technology. It begins with an extensive review of the concept of MOSFET scaling. Two important CMOS device design parameters, threshold voltage and channel length, are then discussed in detail.

CMOS Device Design (Chapter 4) - Fundamentals of Modern ...

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